

SOPHITORIUM ENGINEERING COLLEGE, KHURDA, BBSR

TIME TABLE 3RD SEMESTER BTECH

BRANCH-ELECTRONICS & COMMUNICATION ENGINEERING

DAY	09.00 AM-10.00 AM	10.00 AM-11.00 AM	11.00 AM-12.00 AM	12.00 PM-1.00 PM	1.00 PM-2.00 PM	2.00 PM-3.00 PM	3.00 PM-4.00 PM
MONDAY	Mathematics - III Prof. B.K Mishra R.No-04	Electrical Circuit Analysis Prof. P.K Hota R.No-04	Analog Electronics Circuit Mrs. A Rout R.No-05	Electrical Circuit Analysis Prof. P.K Hota R.No-04	B R E A K	Analog Electronics Circuit Lab Mrs. A Rout	
TUESDAY	Organizational Behaviour Mr. M.D Raj R.No-03	Web and Application Development Ms. G Moharana R.No-03	Analog Electronics Circuit Mrs. A Rout R.No-05	Signal & System Mrs. R.L Das R.No-05		Signal & System Lab Mrs. R.L Das	
WEDNESDAY	Mathematics - III Prof. B.K Mishra R.No-03	Organizational Behaviour Mr. M.D Raj R.No-03	Electrical Circuit Analysis Prof. P.K Hota R.No-04	Web and Application Development Ms. G Moharana R.No-03		Electrical Circuit Analysis Lab Prof. P.K Hota	
THURSDAY	Mathematics - III Prof. B.K Mishra R.No-04	Web and Application Development Ms. G Moharana R.No-03	Organizational Behaviour Mr. M.D Raj R.No-03	Electrical Circuit Analysis Prof. P.K Hota R.No-04		Web and Application Development Lab Ms. G Moharana	
FRIDAY	Mathematics - III Prof. B.K Mishra R.No-04	Analog Electronics Circuit Mrs. A Rout R.No-05	Organizational Behaviour Mr. M.D Raj R.No-03	Electrical Circuit Analysis Prof. P.K Hota R.No-04		Signal & System Mrs. R.L Das R.No-05	Library
SATURDAY	Web and Application Development Ms. G Moharana R.No-03	Signal & System Mrs. R.L Das R.No-05	Signal & System Mrs. R.L Das R.No-05	Analog Electronics Circuit Mrs. A Rout R.No-05	Sports		

COPY TO Principal

All HOD for information please

All faculties for information and necessary action please

Transport & Security Supervisor for information and necessary action please

Notice Board.

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